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(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number 10/840,206

Filing Date May 5, 2004

First Named Inventor Ian Chesal

Art Unit 2123

Examiner Name Unassigned

Attorney Docket Number 015114-074300US

FNCLOSURES (Check all that apply)								
	Fee Transmittal Form  Fee Attached  Amendment/Reply  After Final  Affidavits/declaration(s)  Extension of Time Request  Express Abandonment Request  Information Disclosure Statement  Certified Copy of Priority Document(s)  Reply to Missing Parts/ Incomplete Application  Reply to Missing Parts under 37 CFR 1.52 or 1.53	Drawing(s)  Licensing-related Papers  Petition Petition to Convert to a Provisional Application Change of Correspondence Address Terminal Disclaimer Request for Refund  CD, Number of CD(s)  Landscape Table on CD  After Allowance Communication to of Appeals and Interferer Appeal Communication to of Appeal	o Board ces o TC r Brief) se identify e), copy of one					
	SIGI	IATURE OF APPLICANT, ATTORNEY, OR AGENT						
Firm Name Townsend and		vnsend and Crew LLP						
Printe	d name Steven Cahill							
Date		Reg. No. 44,578						
	CERTIFICATE OF TRANSMISSION/MAILING							

## I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. Signature Typed or printed name Roger Hytter Date Date 12 17 04

Client Reference No.: A1101

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ian Chesal et al.

DEC 2 0 2004

Application No.: 10/840,206

Filed: May 5, 2004

For: TECHNIQUES FOR EDITING CIRCUIT DESIGN FILES TO BE COMPATIBLE WITH A NEW PROGRAMMABLE IC

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Examiner: Unassigned

Art Unit: 2123

INFORMATION DISCLOSURE STATEMENT

UNDER 37 CFR §1.97 and §1.98

Sir:

The reference cited on attached form PTO/SB/08B is being called to the attention of the Examiner. A copy of the reference is enclosed. It is respectfully requested that the cited reference be expressly considered during the prosecution of this application, and the reference be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR §1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the abovenoted Deposit Account.

Respectfully submitted

Steven Cahill Reg. No. 44,578

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Substitute for form 1449B/PTO

Sheet

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

of

Complete if Known 10/840,206 Application Number Filing Date May 5, 2004 First Named Inventor Ian Chesal Art Unit 2123 Examiner Name Unassigned 015114-074300US Attorney Docket Number

Examiner Initials *	Cite No.1	(book, magazine, journal, senai, symposium, caulog, etc.), date, page(o), volume issue nameer(o),		
	1	"Altera Design Flow for Xilinx Users," Application Design Note version 2.0 from Altera Corporation San Jose, CA (July 2003).		

Examiner	Date	
Signature	Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached.